



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 6,194,925 B1 Docket: 12583.5USU1
Issue Date: FEBRUARY 27, 2001 Patentee: KIMSAL ET AL.
Title: TIME INTERVAL MEASUREMENT SYSTEM INCORPORATING A
LINEAR RAMP GENERATION CIRCUIT

CERTIFICATE UNDER 37 CFR 1.8:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Certificate of Correction Branch, Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450 on March 11, 2005.

By:

Name:

2- W > G
Brian H. Batzli

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

23552

PATENT TRADEMARK OFFICE

Sir:

It is requested that a Certificate of Correction be issued correcting errors appearing in the above-identified United States patent. One copy of the text of the Certificate in the suggested form is enclosed.

When the patent application for the above-identified patent was filed, 6 sheets of drawings (Figs. 1, 2, 3, 3A, 3B, 3C and 4) were submitted as part of the patent application. During the course of prosecution, Applicants amended Figs. 3A-3C (Sheets 4-6). However, at the time the issue fee was paid, Applicants submitted formal drawings, but due to a clerical error inadvertently omitted the newly corrected Figs. 3A-3C (Sheets 4-6).

As some of the errors listed are due to Applicant's mistake, our check in the amount of \$100.00 is enclosed to cover the Certificate fee.

03/15/2005 YPOLITE2 00000007 6194925

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100.00 OP

Certificate
MAR 17 2005
of Correction

MAR 21 2005

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims, and re-examination is not required.

Respectfully submitted,

MERCHANT & GOULD P.C.
P.O. Box 2903
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(612) 332-5300

Date: 10 march 2005



Brian H. Batzli
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PATENT NO. : 6,194,925 B1

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DATED : FEBRUARY 27, 2001

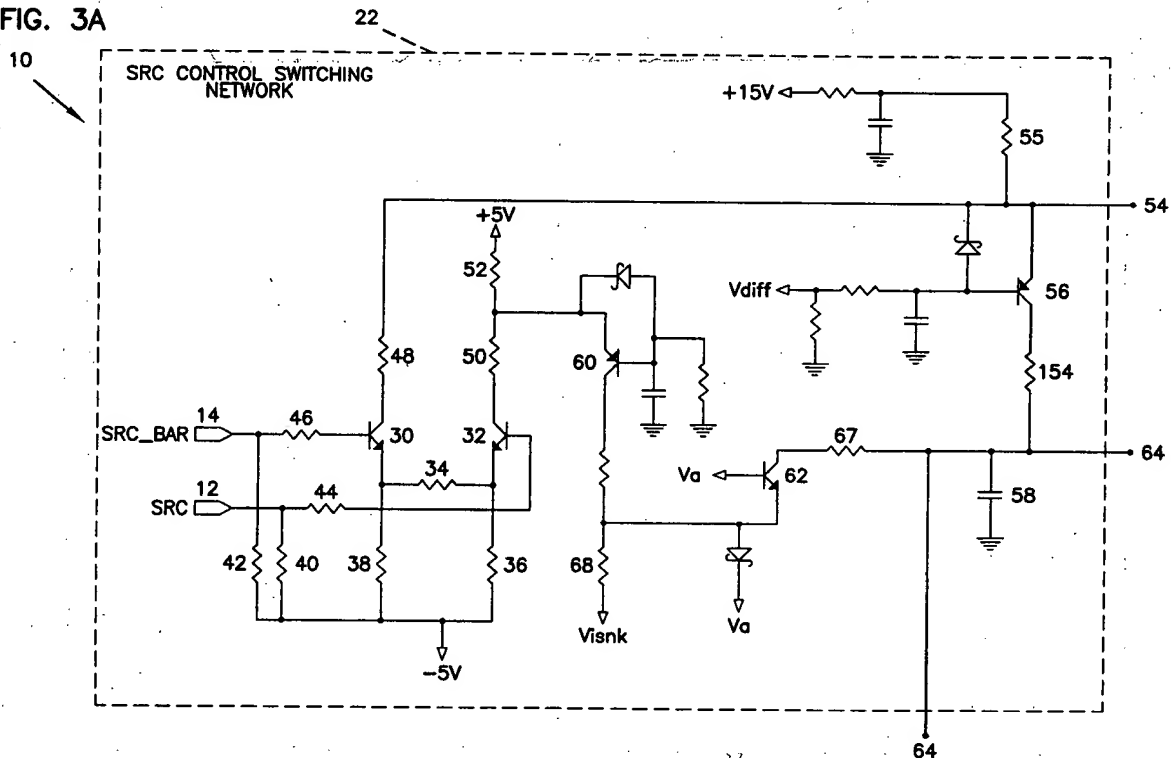
INVENTOR(S) : KIMSAL ET AL.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Drawing Sheets: insert omitted Fig. 3A:

Sheet 4 of 6:

FIG. 3A



MAILING ADDRESS OF SENDER:

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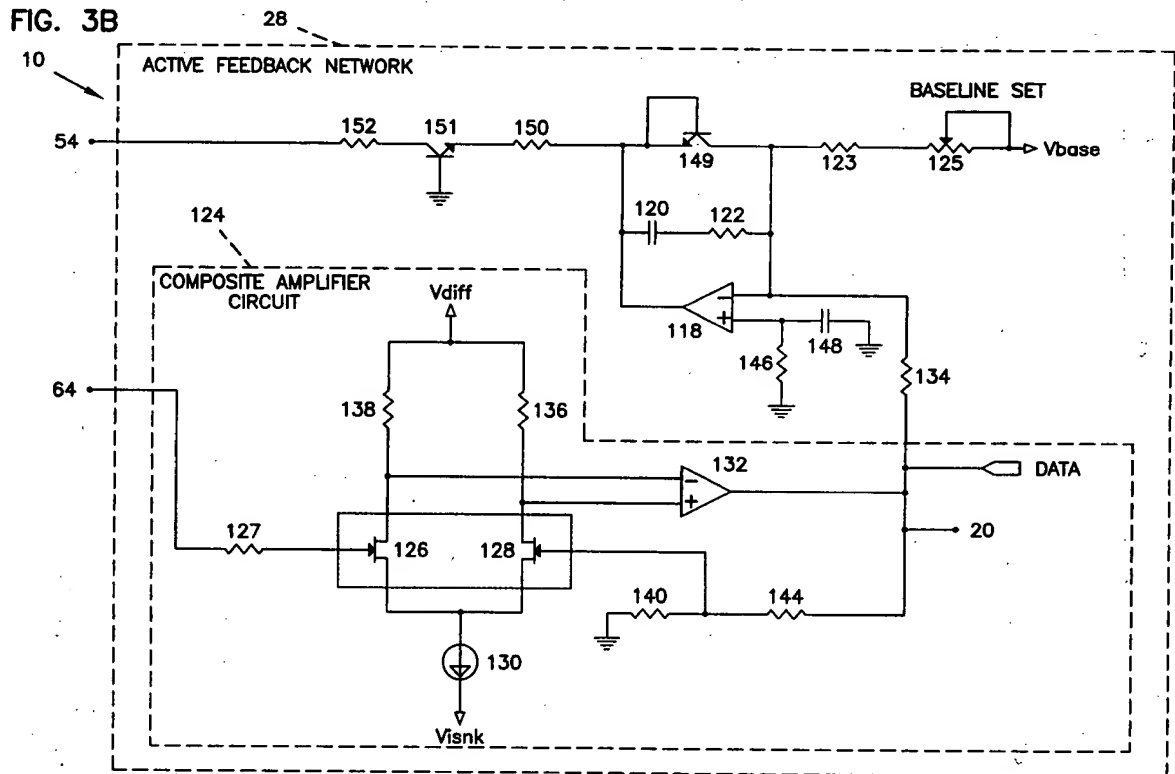
DATED : FEBRUARY 27, 2001

INVENTOR(S) : KIMSAL ET AL.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Drawing Sheets: insert omitted Fig. 3B:

Sheet 5 of 6:



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DATED : FEBRUARY 27, 2001

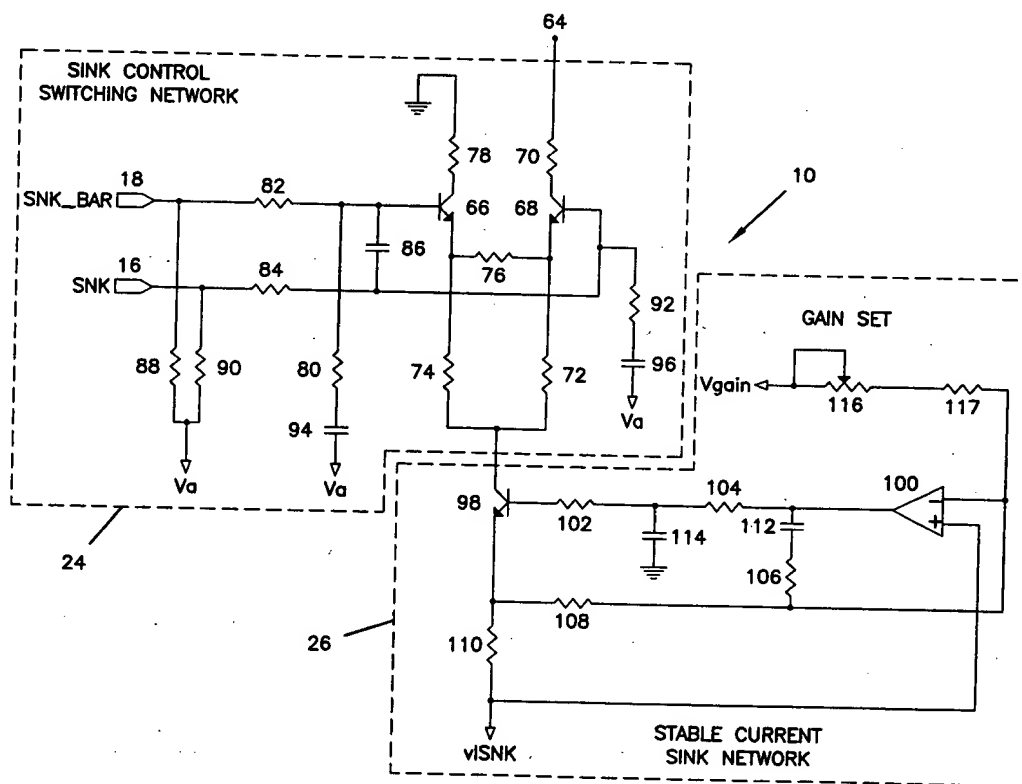
INVENTOR(S) : KIMSAL ET AL.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Drawing Sheets: insert omitted Fig. 3C:

Sheet 6 of 6:

FIG. 3C



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CERTIFICATE OF CORRECTION**

PATENT NO. : 6,194,925 B1

PAGE 4 **of** 4

DATED : FEBRUARY 27, 2001

INVENTOR(S) : KIMSAL ET AL.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 3, line 9: "present invention." should read --present invention;--

Col. 3, lines 41-42: "capacitors 58," should read --capacitor 58,--

Col. 3, line 42: "FIG. 3A. In" should read --FIG. 3A, in--

Col. 6, line 21: "current drawn drawn from" should read --current drawn from--

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FIG. 3A

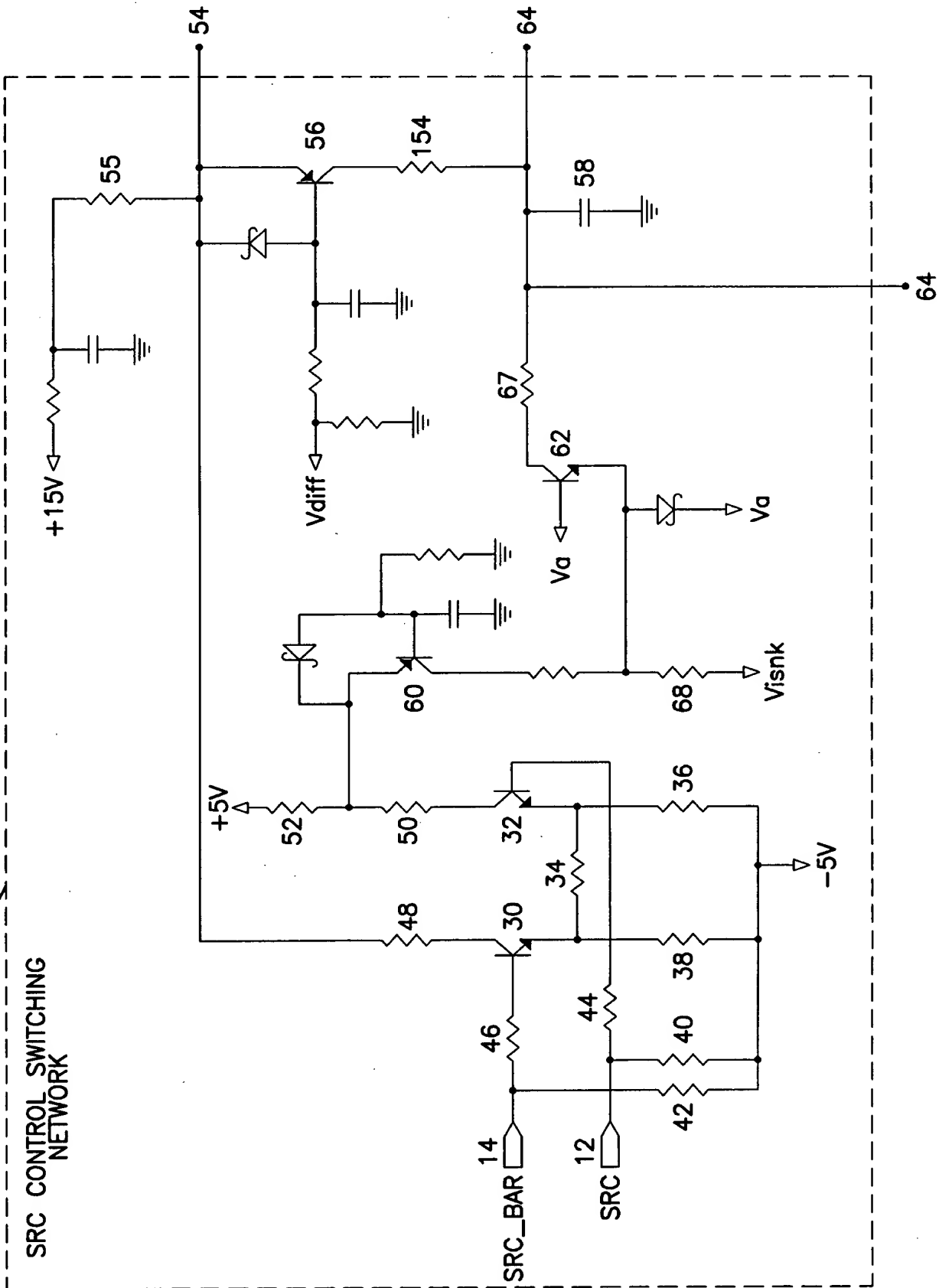


FIG. 3B

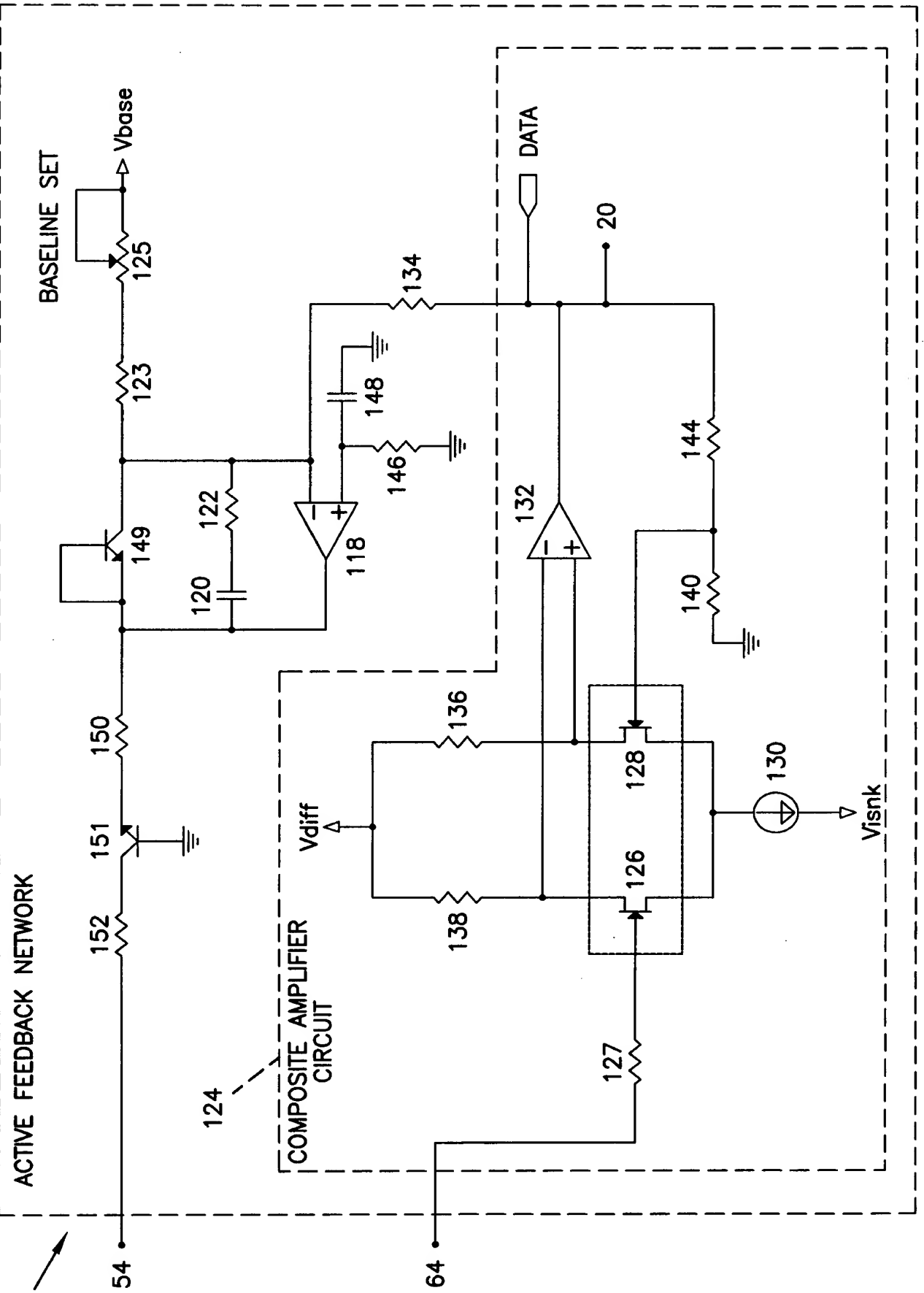


FIG. 3C

